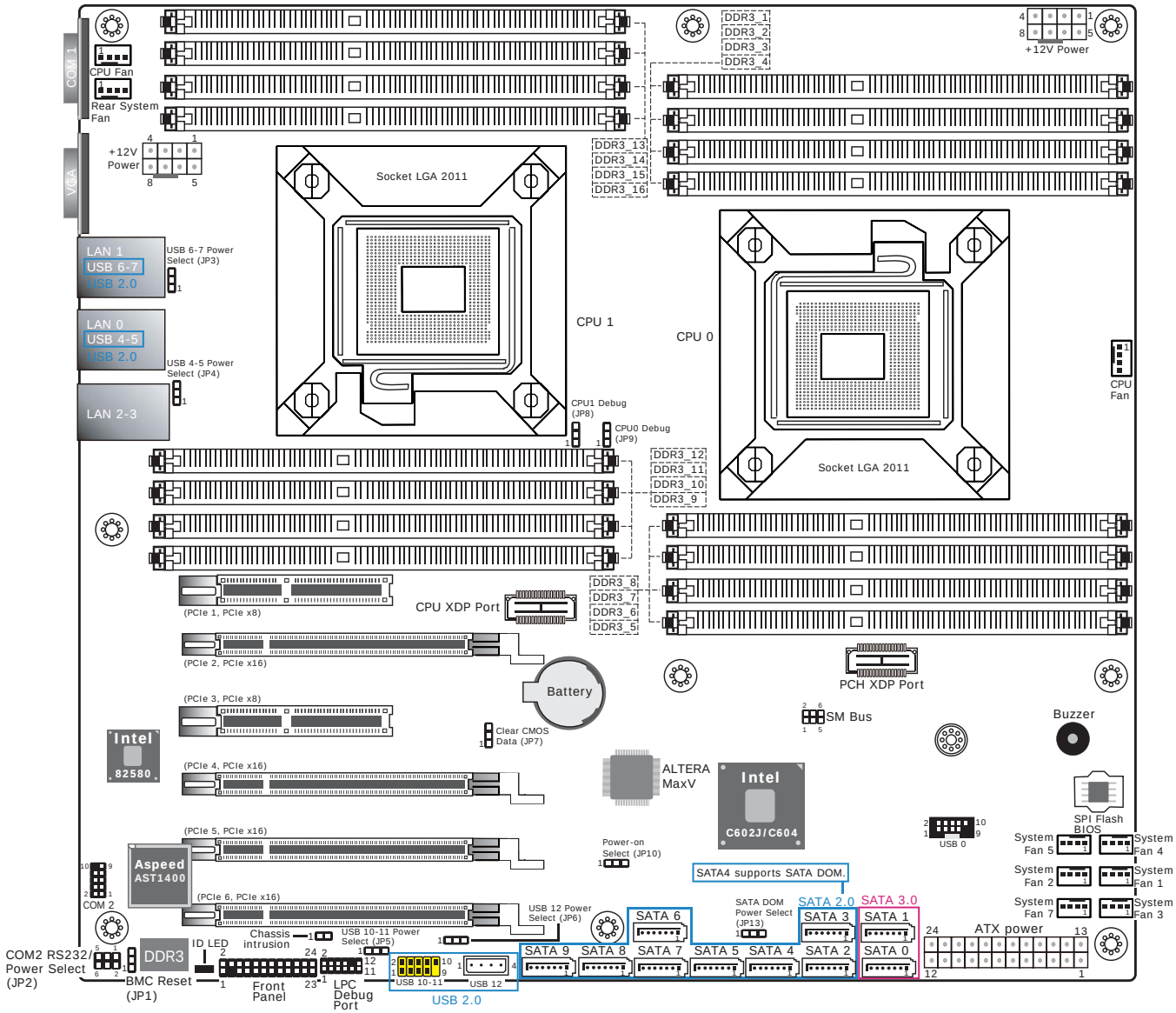
RL830-C602/C604



BMC Reset	JP1
Normal (default)	1-2 On
Reset BMC	2-3 On

USB Power Select: 4-5 (JP4), 6-7 (JP3), 10-11 (JP5), 12 (JP6)	
+5V (default)	1-2 On
+5V_standby	2-3 On

Clear CMOS data	JP7
Normal (default)	1-2 On
Clear CMOS Data	2-3 On

SATA DOM Power Select	JP13
GND (default)	1-2 On
+5V	2-3 On

Power-on Select	JP10
Power-on via AC power (default)	1-2 On
Power-on via power button	2-3 On

COM2 RS232/Power Select	JP2
RS232 (default)	1-3 (RI), 2-4 (DCD) On
RS232 with power	3-5 (+5V), 4-6 (+12V) On

CPU1 Debug	JP8
Force JTAG by pass of CPU1	1-2 On
Normal (default)	2-3 On

CPU0 Debug	JP9
Force JTAG by pass of CPU0	1-2 On
Normal (default)	2-3 On

Note:
1. CPU 0 socket must be populated first.
2. SATA4 supports SATA DOM.

Battery Notice

Battery Usage

The lithium ion battery powers the real-time clock and CMOS memory. It is an auxiliary source of power when the main power is shut off.

Safety Measures

- Danger of explosion if battery incorrectly replaced.
- Replace only with the same or equivalent type recommend by the manufacturer.
- Dispose of used batteries according to local ordinance.

Mesure de Sécurité de l'usage de Batterie

Batterie:

- Danger d'explosion si la batterie n'est pas correctement remplacée.
- Remplacez seulement avec le même type ou équivalent recommandé par le fabricant.
- Traitez des batteries usées selon le règlement local.