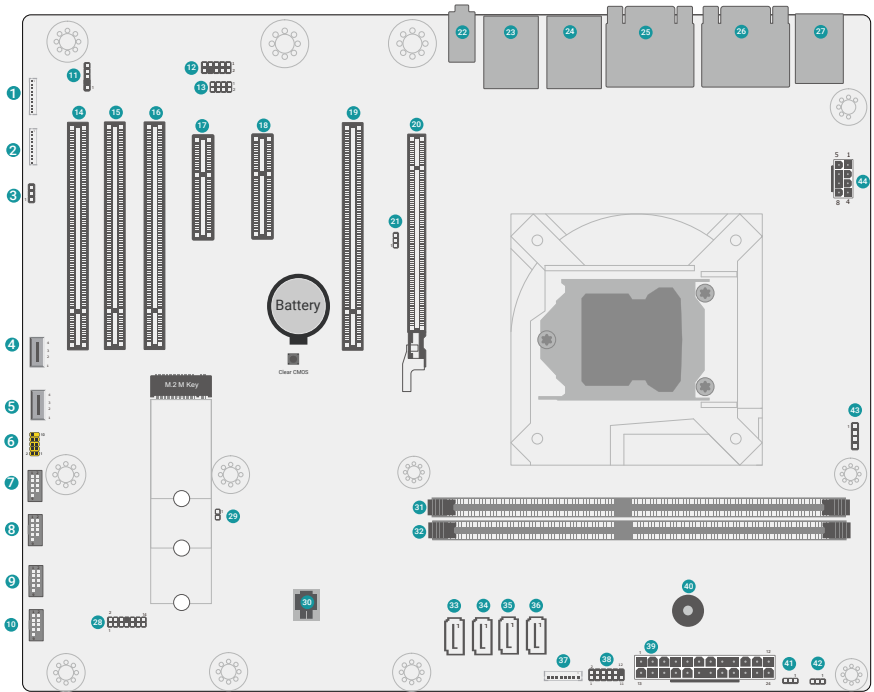


## Board Layout

### Top View



1	DIO1	23	▲2.5G LAN1 ▼USB 3.2 Gen1
2	DIO2	24	▲1G LAN2 ▼USB 3.2 Gen1
3	System Fan1	25	▲COM1 ▼COM2
4	USB2_14	26	▲VGA ▼HDMI
5	USB2_9	27	▲PS/2 (mini-DIN-6) ▼USB2.0
6	USB2_3/4	28	eSPI
7	COM3	29	Case Open
8	COM4	30	SPI
9	COM5	31	DIMM1
10	COM6	32	DIMM2
11	S/PDIF	33	SATA4
12	Front Audio	34	SATA3
13	LAN LED	35	SATA2
14	PCI4	36	SATA1
15	PCI3	37	SMBus
16	PCI2	38	Front Panel
17	PCIE3	39	ATX Power
18	PCIE2	40	Buzzer
19	PCI1	41	System Fan2
20	PCIE1	42	System Fan3
21	CLKREQ/PRSNT Select	43	CPU Fan
22	▲Line-out ▼Mic-in	44	12V Power

## Jumper Settings

21 CLKREQ/PRSNT Select	CLKREQ_SEL1
CLKREQ	1-2 ON
PRSNT(default)	2-3 ON

## PIN Assignment

### 3 ▶ System Fan1 (SYS\_FAN1)

Pin	Assignment
1	GND
2	PWM
3	TACH

### 41 ▶ System Fan2 (SYS\_FAN2)

Pin	Assignment
1	GND
2	PWM
3	TACH

### 42 ▶ System Fan3 (SYS\_FAN3)

Pin	Assignment
1	GND
2	PWM
3	TACH

### 29 ▶ Case Open (C\_INTRUSION1)

Pin	Assignment
1	GND
2	Case Open-

### 43 ► CPU Fan (CPU\_FAN1)

Pin	Assignment
1	GND
2	12V
3	RPM
4	CTRL

### 11 ► S/PDIF (SPDIF\_HEADER1)

Pin	Assignment
1	5V
2	NC
3	SPDIF OUT
4	GND
5	SPDIF IN

### 37 ► SMBus (SMBUS1)

Pin	Assignment	Pin	Assignment
1	3.3V Standby	2	GND
3	SMB CLOCK	4	SMB DATA
5	SMB ALERT_L	6	I2C CLOCK
7	I2C DATA	8	I2C INT

### 13 ► LAN LED Header (JP2)

Pin	Assignment	Pin	Assignment
1	LAN2 SPEED 2.5G	2	LAN2 SPEED 1G
3	LAN2 ACTIVE	4	3.3V_LAN2
5	LAN1 SPEED 1G	6	LAN1 SPEED 100M
7	LAN1 ACTIVE	8	3.3V_LAN1

## 7 ► COM3 (COM3)

Pin	Assignment	Pin	Assignment
1	MDCD-	2	MSIN-
3	MSO-	4	MDTR-
5	GND	6	MDSR-
7	MRTS-	8	MCTS-
9	MRI-/5V/12V	10	----

## 8 ► COM4 (COM4)

Pin	Assignment	Pin	Assignment
1	MDCD-	2	MSIN-
3	MSO-	4	MDTR-
5	GND	6	MDSR-
7	MRTS-	8	MCTS-
9	MRI-/5V/12V	10	----

## 9 ► COM5 (COM5)

Pin	Assignment	Pin	Assignment
1	MDCD-	2	MSIN-
3	MSO-	4	MDTR-
5	GND	6	MDSR-
7	MRTS-	8	MCTS-
9	MRI-/5V/12V	10	----

## 10 ► COM6 (COM6)

Pin	Assignment	Pin	Assignment
1	MDCD-	2	MSIN-
3	MSO-	4	MDTR-
5	GND	6	MDSR-
7	MRTS-	8	MCTS-
9	MRI-/5V/12V	10	----

## 12 ► Front Audio (F\_AUDIO1)

Pin	Assignment	Pin	Assignment
1	MIC2_L	2	A_GND
3	MIC2_R	4	HP_MIC_COMBO
5	LINE2_R	6	MIC2_JD
7	A_GND	8	NC
9	LINE2_L	10	LINE2_JD

## 6 ► USB2\_3/4 (JP12)

Pin	Assignment	Pin	Assignment
1	USB_VCC	2	USB_VCC
3	USB_N3	4	USB_N4
5	USB_P3	6	USB_P4
7	GND	8	GND
9	----	10	NC

## 1 ► DIO\_Connector1 (J1)

Pin	Assignment
1	DI_3
2	DI_2
3	DI_1
4	DI_0
5	DO_3
6	DO_2
7	DO_1
8	DO_0
9	5V
10	GND

## 2 ► DIO\_Connector2 (J2)

Pin	Assignment
1	NC
2	NC
3	NC
4	NC
5	DO_7
6	DO_6
7	DO_5
8	DO_4
9	5V
10	GND

## 38 ► Front Panel (F\_PANEL1)

Pin	Assignment	Pin	Assignment
1	NC	2	V_SUS_LED
3	3V3	4	V_SUS_LED
5	HDD_LED	6	SUS_LED
7	GND	8	GND
9	SYS_RST-	10	PWR_BTN-
11	NC	12	---

## 28 ► eSPI (ESPI\_DEBUG1)

Pin	Assignment	Pin	Assignment
1	3V3SB	2	ESPI_CLK
3	ESPI_RESET#	4	GND
5	ESPI_ALT#	6	UART_TX
7	ESPI_D0	8	---
9	ESPI_D1	10	ESPI_CS
11	ESPI_D2	12	UART_RX
13	ESPI_D3	14	3V3SB

**36 ▶ SATA1 (SATA0-R7)**

Pin	Assignment
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND

**35 ▶ SATA2 (SATA1-R6)**

Pin	Assignment
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND

**34 ▶ SATA3 (SATA2-R5)**

Pin	Assignment
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND

**33 ▶ SATA4 (SATA3-R4)**

Pin	Assignment
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND



DFI reserves the right to change the specifications at any time prior to the product's release. This QR may be based on the product's revision. For more documentation and drivers, please visit the download page at [www.dfi.com/downloadcenter](http://www.dfi.com/downloadcenter), or via the QR codes to the right.

