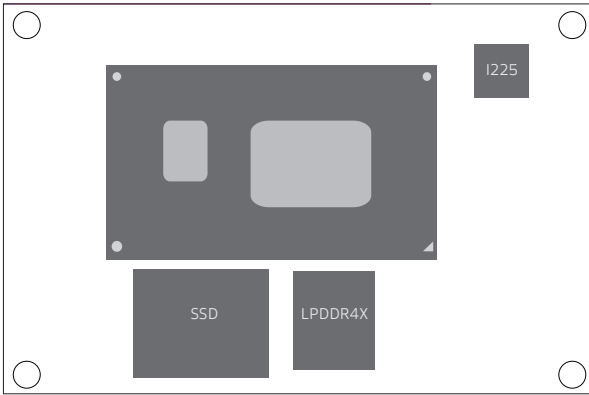
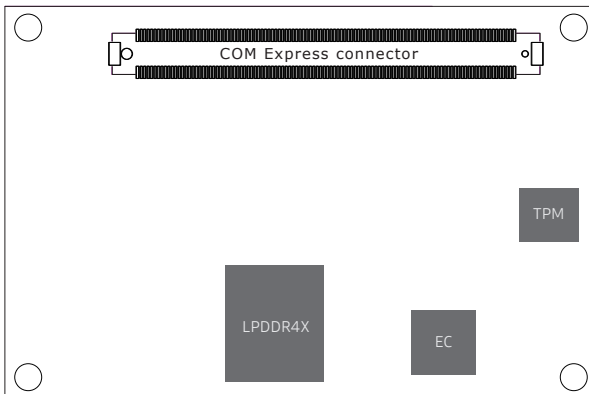


## Board Layout and COM Express Pin Assignments

### Top View



### Bottom View



Pin	Row A	Pin	Row B
A1	GND (FIXED)	B1	GND (FIXED)
A2	GBE0_MDI3-	B2	GBE0_ACT#
A3	GBE0_MDI3+	B3	LPC_FRAME#/ESPL_CS0#
A4	Note*GBE0_LINK100#	B4	LPC_AD0/ESPL_IO_0
A5	Note**GBE0_LINK100#	B5	LPC_AD1/ESPL_IO_1
A6	GBE0_MDI2-	B6	LPC_AD2/ESPL_IO_2
A7	GBE0_MDI2+	B7	LPC_AD3/ESPL_IO_3
A8	GBE0_LINK#	B8	LPC_DRQ0#/ESPL_ALERT0#
A9	GBE0_MDI1-	B9	LPC_DRQ1#/ESPL_ALERT1#
A10	GBE0_MDI1+	B10	LPC_CLK/ESPL_CK
A11	GND (FIXED)	B11	GND (FIXED)
A12	GBE0_MDI0-	B12	PWRBTN#
A13	GBE0_MDI0+	B13	SMB_CK
A14	GBE0_CTREF	B14	SMB_DAT
A15	SUS_S3#	B15	SMB_ALERT#
A16	SATA0_TX+	B16	SATA1_TX+
A17	SATA0_TX-	B17	SATA1_TX-
A18	SUS_S4#	B18	SUS_STAT#/ESPL_RESET#
A19	SATA0_RX+	B19	SATA1_RX+
A20	SATA0_RX-	B20	SATA1_RX-
A21	GND (FIXED)	B21	GND (FIXED)
A22	USB_SSRX0-	B22	USB_SSTX0-
A23	USB_SSRX0+	B23	USB_SSTX0+
A24	SUS_S5#	B24	PWR_OK
A25	USB_SSRX1-	B25	USB_SSTX1-
A26	USB_SSRX1+	B26	USB_SSTX1+
A27	BATLOW#	B27	WDT
A28	(S)ATA_ACT#	B28	HDA_SDIN2
A29	HDA_SYNC	B29	HDA_SDIN1
A30	HDA_RST#	B30	HDA_SDIN0
A31	GND (FIXED)	B31	GND (FIXED)
A32	HDA_BITCLK	B32	SPKR
A33	HDA_SDOUT	B33	I2C_CK
A34	BIOS_DIS0#/ESPL_SAFS	B34	I2C_DAT
A35	THRMTRIP#	B35	THRM#
A36	USB6-	B36	USB7-
A37	USB6+	B37	USB7+
A38	USB_6_7_OC#	B38	USB_4_5_OC#
A39	USB4-	B39	USB5-

Pin	Row A	Pin	Row B
A40	USB4+	B40	USB5+
A41	GND (FIXED)	B41	GND (FIXED)
A42	USB2-	B42	USB3-
A43	USB2+	B43	USB3+
A44	USB_2_3_OC#	B44	USB_0_1_OC#
A45	USB0-	B45	USB1-
A46	USB0+	B46	USB1+
A47	VCC_RTC	B47	ESPL_EN#
A48	RSVD	B48	USB0_HOST_PRSENT
A49	GBE0_SDP	B49	SYS_RESET#
A50	LPC_SERIRQ/ESPI_CS1#	B50	CB_RESET#
A51	GND (FIXED)	B51	GND (FIXED)
A52	RSVD	B52	RSVD
A53	RSVD	B53	RSVD
A54	GPIO	B54	GPO1
A55	RSVD	B55	RSVD
A56	RSVD	B56	RSVD
A57	GND	B57	GPO2
A58	PCIE_TX3+	B58	PCIE_RX3+
A59	PCIE_TX3-	B59	PCIE_RX3-
A60	GND (FIXED)	B60	GND (FIXED)
A61	PCIE_TX2+	B61	PCIE_RX2+
A62	PCIE_TX2-	B62	PCIE_RX2-
A63	GPIO	B63	GPO3
A64	PCIE_TX1+	B64	PCIE_RX1+
A65	PCIE_TX1-	B65	PCIE_RX1-
A66	GND	B66	WAKE0#
A67	GPIO	B67	WAKE1#
A68	PCIE_TX0+	B68	PCIE_RX0+
A69	PCIE_TX0-	B69	PCIE_RX0-
A70	GND (FIXED)	B70	GND (FIXED)
A71	eDP_TX2+	B71	DDIO_PAIR0+
A72	eDP_TX2-	B72	DDIO_PAIR0-
A73	eDP_TX1+	B73	DDIO_PAIR1+
A74	eDP_TX1-	B74	DDIO_PAIR1-
A75	eDP_TX0+	B75	DDIO_PAIR2+
A76	eDP_TX0-	B76	DDIO_PAIR2-
A77	eDP_VDD_EN	B77	DDIO_PAIR4+

**Note:**



1. \*GBE\_LED\_100# is active during a 1Gb connection.
2. \*\*GBE\_LED\_1000# is active during a 2.5Gb connection.

Pin	Row A	Pin	Row B
A78	NC	B78	DDIO_PAIR4-
A79	NC	B79	eDP_BKLT_EN
A80	GND (FIXED)	B80	GND (FIXED)
A81	eDP_TX3+	B81	DDIO_PAIR3+
A82	eDP_TX3-	B82	DDIO_PAIR3-
A83	eDP_AUX+	B83	eDP_BKLT_CTRL
A84	eDP_AUX-	B84	VCC_5V_SBY
A85	GPI3	B85	VCC_5V_SBY
A86	RSVD	B86	VCC_5V_SBY
A87	eDP_HPD	B87	VCC_5V_SBY
A88	PCIE0_CK_REF+	B88	BIOS_DIS1#
A89	PCIE0_CK_REF-	B89	DDIO_HPD
A90	GND (FIXED)	B90	GND (FIXED)
A91	SPL_POWER	B91	DDIO_PAIR5+
A92	SPL_MISO	B92	DDIO_PAIR5-
A93	GPO0	B93	DDIO_PAIR6+
A94	SPL_CLK	B94	DDIO_PAIR6-
A95	SPL_MOSI	B95	DDIO_DDC_AUX_SEL
A96	TPM_PP	B96	USB7_HOST_PRSENT
A97	TYPE10#	B97	SPL_CS#
A98	SER0_TX	B98	DDIO_CTRLCLK_AUX+
A99	SER0_RX	B99	DDIO_CTRLDATA_AUX-
A100	GND (FIXED)	B100	GND (FIXED)
A101	SER1_TX	B101	FAN_PWNOUT
A102	SER1_RX	B102	FAN_TACHIN
A103	LID#	B103	SLEEP#
A104	VCC_12V	B104	VCC_12V
A105	VCC_12V	B105	VCC_12V
A106	VCC_12V	B106	VCC_12V
A107	VCC_12V	B107	VCC_12V
A108	VCC_12V	B108	VCC_12V
A109	VCC_12V	B109	VCC_12V
A110	GND (FIXED)	B110	GND (FIXED)



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