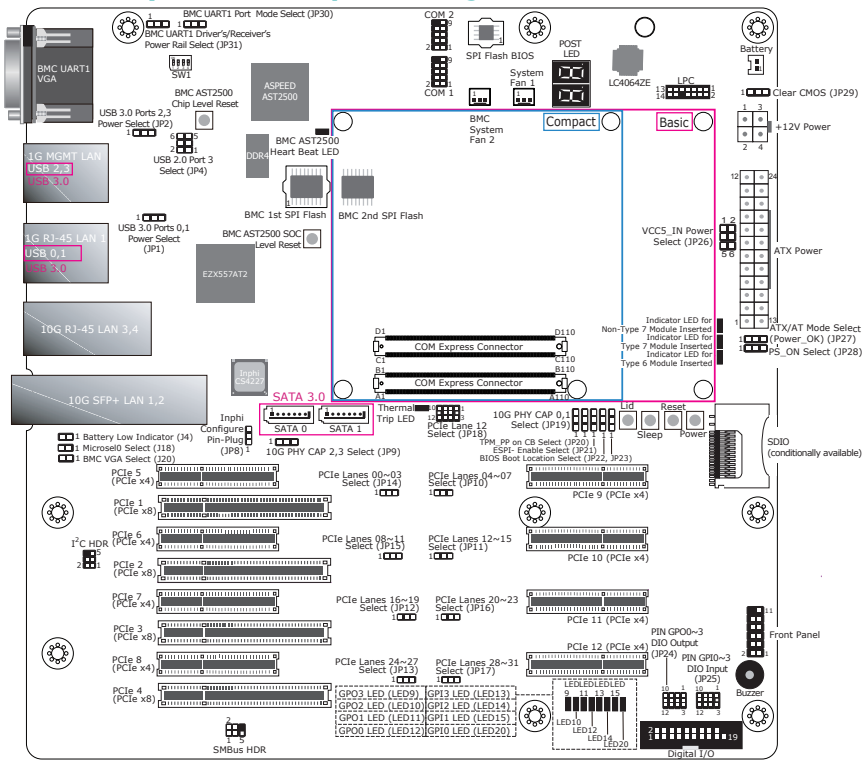


Board Layout and Jumper Settings



TPM_PP on CB Select	JP20
On	1-2 On
Off (default)	2-3 On

ESPI- Enable Select	JP21
LPC BUS Enable (default)	1-2 On
ESPI- BUS Enable	2-3 On

VCC5_IN Power Select	JP26
+5V_standby (default)	1-2 On
+5V	3-4 On
NC	5-6 On

ATX/AT Mode Select (Power_OK)	JP27
PWR_OK from ATX PS PWR_OK (default)	1-2 On
PWR_OK from BMC GPO	2-3 On
No PWR_OK when in AT Mode	1-X On

PS_ON Select	JP28
ATX Mode Auto PWR_ON (default)	1-2 On
AT Mode Always ON	2-3 On

BMC UAR1 Port Mode Select	JP30
Normal UART	1-2 On
Debug Mode (default)	2-3 On

BMC UAR1 Driver's/Receiver's Power Rail Select	JP31
3V3 (Normal)	1-2 On
3VDU (Debug) (default)	2-3 On

BMC SPI1 Interface Mode Select	SW1 Pin 1	SW1 Pin 2
Disable SPI Interface (default)	Off	Off
Enable SPI Master	Off	On
Reserved	On	Off
Enable SPI Pass Through	On	On

BMC 2nd Boot Watchdog Timer Select	SW1 Pin 3
Disable	Off
Enable (default)	On
BMC MAC#1 Interface Type Select	SW1 Pin 4
RMII/NCSE	Off
RGII (default)	On

BIOS Boot Location Select	JP22	JP23
SPIO on Module (default)	2-3 On	2-3 On
SPIO on Carrier	2-3 On	1-2 On
Module SPI Only	1-2 On	1-2 On

Clear CMOS	JP29
Normal (default)	1-2 On
Clear CMOS	2-3 On

BMC VGA Select	J20
Disable VGA	1-2 On
Normal VGA/2D (default)	1-2 Off

Battery Low Indicator	J4
Battery Low Test	1-2 On
Normal (default)	1-2 Off

Note:

1. JP27 and JP28 need to be set in the similar mode.
2. JP30 and JP31 need to be set simultaneously.

Pin Assignments

Front Panel

	Pin	Assignment		Pin	Assignment
	1	NC		2	LED Power
	3	HDD Power	PWR-LED	4	LED Power
HD-LED	5	Signal		6	Signal
	7	GND	ATX-SW	8	GND
RESET	9	Signal		10	Signal
	11	NC		12	---

+12V Power

Pin	Assignment	Pin	Assignment
1	GND	3	+12V
2	GND	4	+12V

Battery

Pin	Assignment
1	+3.0V
2	GND

Note:

Configurations including but are not limited to PCIe Lane may vary from modules installed.

USB 3.0 Power Select: Ports 0,1 (JP1), Ports 2,3 (JP2)	
+5V (default)	1-2 On
+5VDU	2-3 On

USB 2.0 Port 3 Select	JP4
USB 2.0 Port 3 to Rear I/O Connector	1-3, 2-4 On
USB 2.0 Port 3 to BMC (default)	3-5, 4-6 On

PCIe Lanes 00~03 Select (JP14)	PCIe Lanes 04~07 Select (JP10)	PCIe Lanes 08~11 Select (JP15)	PCIe Lanes 12~15 Select (JP11)	PCIe Lanes 16~19 Select (JP12)	PCIe Lanes 20~23 Select (JP16)	PCIe Lanes 24~27 Select (JP13)	PCIe Lanes 28~31 Select (JP17)	Jumper Setting
PCIe5 x4 Slot (default)	PCIe9 x4 Slot (default)	PCIe6 x4 Slot (default)	PCIe10 x4 Slot (default)	PCIe7 x4 Slot	PCIe11 x4 Slot	PCIe8 x4 Slot (default)	PCIe12 x4 Slot (default)	1-2 On
PCIe1 x8 Slot	PCIe1 x8 Slot	PCIe2 x8 Slot	PCIe2 x8 Slot	PCIe3 x8 Slot (default)	PCIe3 x8 Slot (default)	PCIe4 x8 Slot	PCIe4 x8 Slot	2-3 On

PIN GPO0~3: DIO Output (JP24)	
LED on: low (default)	2-3, 5-6, 8-9, 11-12 On
LED off: high	1-2, 4-5, 7-8, 10-11 On

PCIe Lane 12 Select	JP18
PCIe Lane 12 to BMC (default)	1-2, 4-5, 7-8, 10-11 On
PCIe Lane 12 to Multiplexer (Refer JP11)	2-3, 5-6, 8-9, 11-12 On

10G PHY Capability Select: Lanes 0,1 (JP19), Lanes 2,3 (JP9)	
MDIO only (default)	1-2 On
I ² C or MDIO	2-3 On

PIN GPIO~3: DIO Input (JP25)	
LED on: low (default)	2-3, 5-6, 8-9, 11-12 On
LED off: high	1-2, 4-5, 7-8, 10-11 On

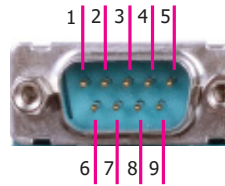
ATX Power

Pin	Assignment	Pin	Assignment
1	+3.3V	13	+3.3V
2	+3.3V	14	-12V
3	GND	15	GND
4	+5V	16	PS_ON#
5	GND	17	GND
6	+5V	18	GND
7	GND	19	GND
8	PWR_OK	20	NC
9	+5VSB	21	+5V
10	+12V	22	+5V
11	+12V	23	+5V
12	+3.3V	24	GND

Digital I/O

Pin	Assignment	Pin	Assignment
1	GND	2	+12V
3	DIO7 (GPO3)	4	+12V
5	DIO6 (GPO2)	6	GND
7	DIO5 (GPO1)	8	+3.3V
9	DIO4 (GPO0)	10	+3.3V
11	DIO3 (GPI3)	12	GND
13	DIO2 (GPI2)	14	+5VSB
15	DIO1 (GPI1)	16	+5VSB
17	DIO0 (GPI0)	18	GND
19	GND	20	---

BMC UART1/Debug: RS232



Pin	Assignment
1	DCD-
2	RD
3	TD
4	DTR-
5	GND
6	DSR-
7	RTS-
8	CTS-
9	RI-

COM 1/2

Pin	Assignment	Pin	Assignment
1	NC	2	RX
3	TX	4	NC
5	GND	6	NC
7	NC	8	NC
9	NC	10	---

Inphi Configure Pin-Plug

Pin	Assignment
1	CS4227 SCL (1.8V)
2	GND
3	CS4227 SDA (1.8V)

SMBus HDR

Pin	Assignment	Pin	Assignment
1	3V3DU	2	GND
3	SMB_CLK	4	SMB_DATA
5	SMB_ALERT#	6	---

SATA 3.0 0/1

Pin	Assignment
1	GND
2	TXP
3	TXN
4	GND
5	RXN
6	RXP
7	GND

COM Express Connectors

Please refer pin assignments in the user's manual at go.dfi.com/COM333-1

System Fan 1/BMC System Fan 2

Pin	Assignment
1	GND
2	Power
3	Sense

I²C HDR

Pin	Assignment	Pin	Assignment
1	3V3DU	2	GND
3	I ² C_CLK	4	I ² C_DATA
5	NC	6	---

LPC

Pin	Assignment	Pin	Assignment
1	L_CLK	2	L_AD1
3	L_RST#	4	L_AD0
5	L_FRAME#	6	3V3
7	L_AD3	8	GND
9	L_AD2	10	---
11	L_SERIRQ	12	CLK_48MHz
13	5VSB	14	5V



DFI reserves the right to change the specifications at any time prior to the product's release. For the latest information about the product, please refer to the user's manual at go.dfi.com/COM333-1.

