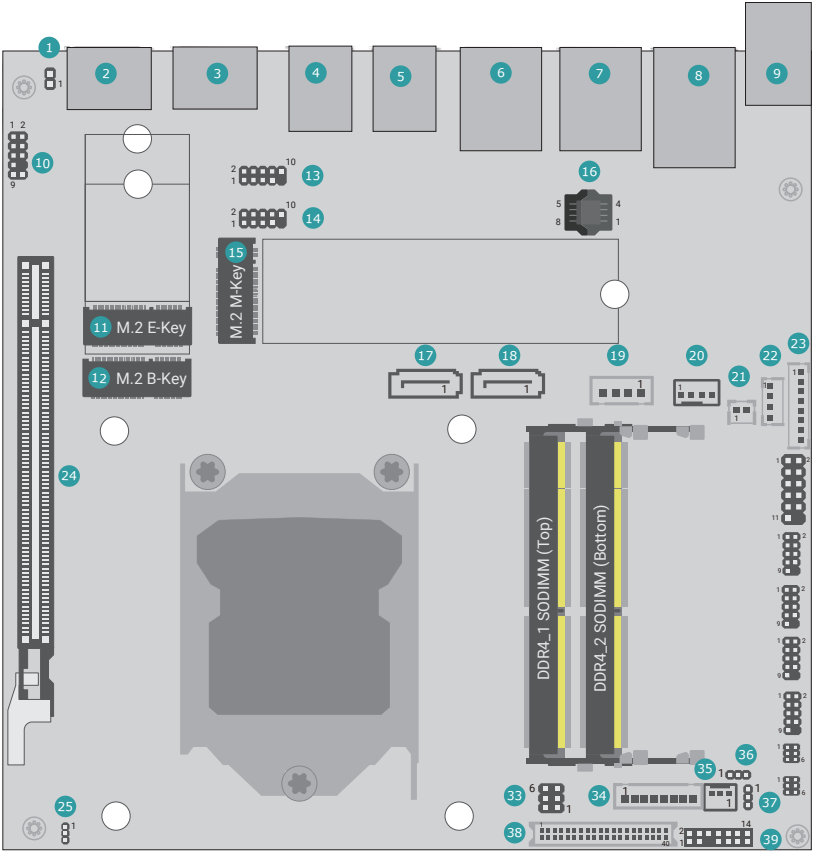


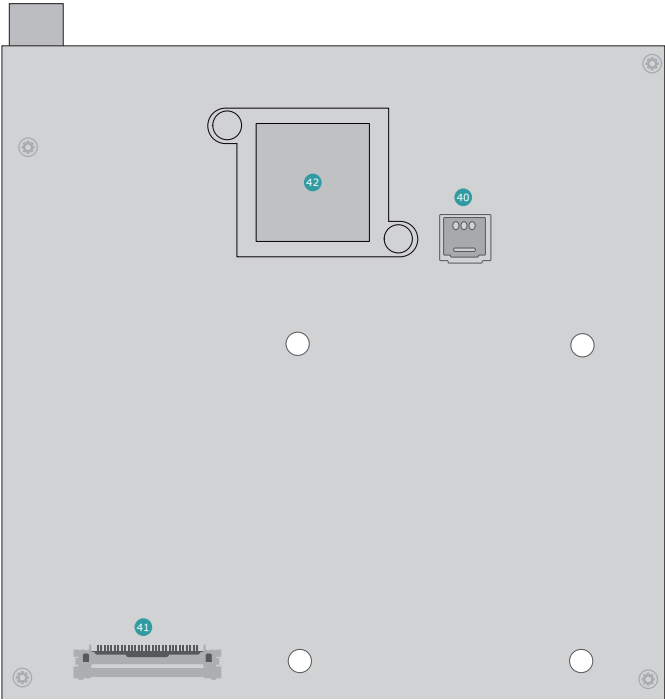
## Board Layout

### Top View



1	Chassis Intrusion	21	Battery
2	DP2	22	DIO Power
3	DP1	23	DIO
4	USB2_3/4	24	PCIE 1
4	USB3_3/4	25	Clear CMOS
5	USB2_1/2	26	Front Panel
5	USB3_1/2	27	COM1
6	LAN3	28	COM2
7	LAN2	29	COM3
8	LAN1	30	COM4
9	DC-IN Single 12V +/-10% DC (CMS101) Wide Range 15~36V (CMS103)	31	Power Selection COM 1
10	Front Audio	32	Power Selection COM 2
11	M.2 2230 E key	33	Panel Power Select
12	M.2 2242/3042/3052 B key	34	LVDS Power
13	USB2_7/8	35	SYS FAN
14	USB2_5/6	36	LCD/Inverter Power Select
15	M.2 2280 M key	37	Backlight Voltage Select
16	SPI	38	LVDS
17	SATA 3.0	39	LPC
18	SATA 3.0		
19	SATA Power		
20	CPU FAN		

# Bottom View



- 40 Nano SIM slot
- 41 eDP
- 42 Intel® W480/Q470/H420 Chipset

# Jumper Settings

<b>25 Clear CMOS</b>	<b>JP1</b>
Normal (default)	1-2 On
Clear CMOS	2-3 On

<b>31 Power Selection COM 1</b>	<b>JP2</b>
RS232 Standard (default)	1-3 / 2-4
RS232 with Power	3-5 / 4-6

<b>32 Power Selection COM 2</b>	<b>JP3</b>
RS232 Standard (default)	1-3 / 2-4
RS232 with Power	3-5 / 4-6

<b>36 LCD/Inverter Power Select</b>	<b>DPJP602</b>
+12V (default)	1-2 On
+5V	2-3 On

<b>37 Backlight Voltage Select</b>	<b>DPJP601</b>
3V3 (Default)	1-2 On
5V	2-3 On

<b>33 Panel Power Select</b>	<b>DPJP603</b>
12V	1-2 On
5V	3-4 On
3.3V (default)	5-6 On

# Pin Assignment

## 26 Front Panel

	Pin	Assignment		Pin	Assignment
	1	NC		2	3V3SB
HD-LED	3	3V3	PWR-LED	4	3V3SB
	5	HD_LED		6	SUS_LED
RESET	7	GND	ATX-SW	8	GND
	9	PM_SYSRST		10	PWR_BTN-
	11	NC		12	---

## 27 28 COM 1/2 Headers: RS232/422/485

## 29 30 COM 3/4 Headers: RS232

Pin	RS232	RS422 Full Duplex	RS485
1	DCD-	TX-	DATA-
2	RD	TX+	DATA+
3	TD	RX+	NC
4	DTR-	RX-	NC
5	GND	GND	GND
6	DSR-	NC	NC
7	RTS-	NC	NC
8	CTS-	NC	NC
9	RI-	NC	NC

**17 18 SATA 3.0 0(R2)/1(R3)**

Pin	Assignment
1	GND
2	TX+
3	TX-
4	GND
5	RX-
6	RX+
7	GND

**23 Digital I/O**

Pin	Assignment
1	DIO0
2	DIO1
3	DIO2
4	DIO3
5	DIO4
6	DIO5
7	DIO6
8	DIO7

**19 SATA Power**

Pin	Assignment
1	+12V
2	GND
3	GND
4	5V

**22 Digital I/O Power**

Pin	Assignment
1	+12V
2	GND
3	5VSB
4	+5V

**21 Battery**

Pin	Assignment
1	+3.3V
2	GND

**1 Chassis Intrusion**

Pin	Assignment
1	Signal
2	GND

#### 41 eDP (optional)

Pin	Assignment	Pin	Assignment
1	NC	21	eDP_PANEL_PWR
2	VCC_INV_PWR	22	eDP_PANEL_PWR
3	VCC_INV_PWR	23	eDP_PANEL_PWR
4	VCC_INV_PWR	24	GND
5	VCC_INV_PWR	25	eDP_AUXN_C
6	NC	26	eDP_AUXP_C
7	NC	27	GND
8	DIMMING	28	eDP_LANE0_P
9	BLONOFF	29	eDP_LANE0_N
10	GND	30	GND
11	GND	31	eDP_LANE1_P
12	GND	32	eDP_LANE1_N
13	GND	33	GND
14	eDP_HDP_C	34	eDP_LANE2_P
15	GND	35	eDP_LANE2_N
16	GND	36	GND
17	GND	37	eDP_LANE3_P
18	GND	38	eDP_LANE3_N
19	NC	39	GND
20	eDP_PANEL_PWR	40	NC

#### 38 LVDS LCD Panel

Pin	Assignment	Pin	Assignment
1	GND	2	GND
3	LVDSA_3+	4	LVDSB_3+
5	LVDSA_3-	6	LVDSB_3-
7	GND	8	GND
9	LVDSA_2+	10	LVDSB_2+
11	LVDSA_2-	12	LVDSB_2-
13	GND	14	GND
15	LVDSA_1+	16	LVDSB_1+
17	LVDSA_1-	18	LVDSB_1-
19	GND	20	GND
21	LVDSA_0+	22	LVDSB_0+
23	LVDSA_0-	24	LVDSB_0-
25	GND	26	GND
27	LVDSA_CLK+	28	LVDSB_CLK+
29	LVDSA_CLK-	30	LVDSB_CLK-
31	GND	32	GND
33	DDC_CLK	34	NC
35	DDC_DATA	36	+3.3V
37	Panel Power	38	Panel Power
39	Panel Power	40	Panel Power

### 34 LVDS Power

Pin	Assignment
1	GND
2	GND
3	Panel Inverter Brightness Voltage Control
4	Panel Power
5	+3.3V
6	Panel Backlight On/Off Control
7	LCD/Inverter Power
8	LCD/Inverter Power

### 10 Front Audio

Pin	Assignment	Pin	Assignment
1	Mic2-L	2	GND
3	Mic2-R	4	NC
5	Line2-R	6	Mic2-JD
7	GND	8	---
9	Line2-L	10	Line2-JD

### 39 LPC

Pin	Assignment	Pin	Assignment
1	CLK	2	LAD1
3	RST#	4	LAD0
5	FRAME#	6	3V3
7	LAD3	8	GND
9	LAD2	10	---
11	SERIRQ	12	GND
13	5VSB	14	5V

### 13 14 USB 2.0 5-6/7-8 Headers

Pin	Assignment	Pin	Assignment
1	PWR	2	PWR
3	DATA-	4	DATA-
5	DATA+	6	DATA+
7	GND	8	GND
9	---	10	NC

### 20 CPU Fan

Pin	Assignment
1	GND
2	12V
3	Sense
4	Speed Control



DFI reserves the right to change the specifications at any time prior to the product's release. This QR may be based on the product's revision. For more documentation and drivers, please visit the download page at [www.dfi.com/downloadcenter](http://www.dfi.com/downloadcenter), or via the QR codes to the right.

