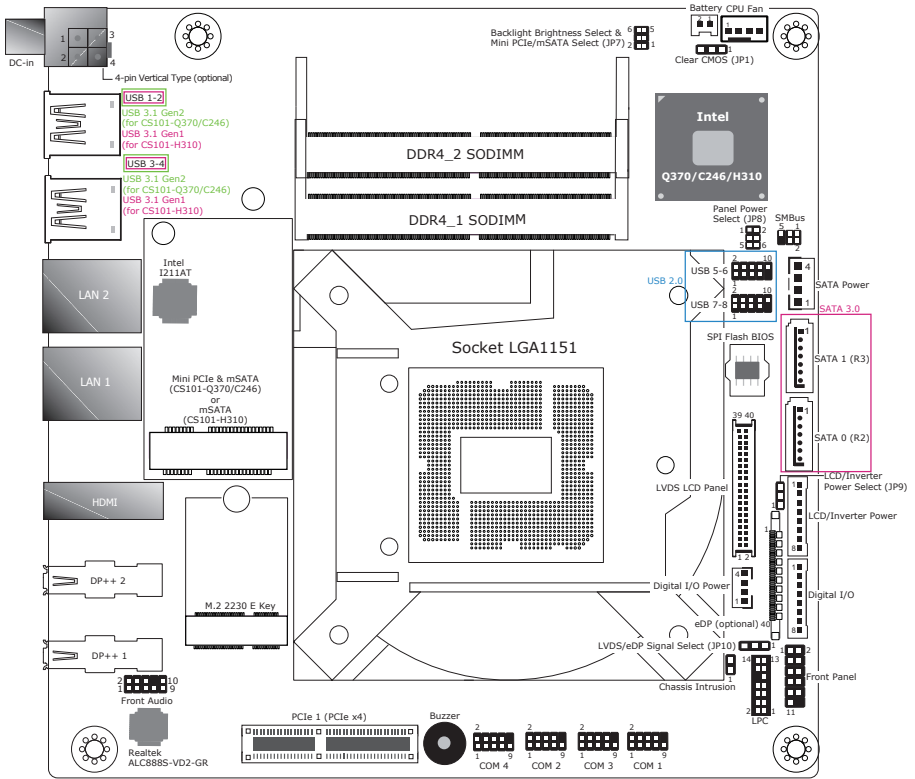
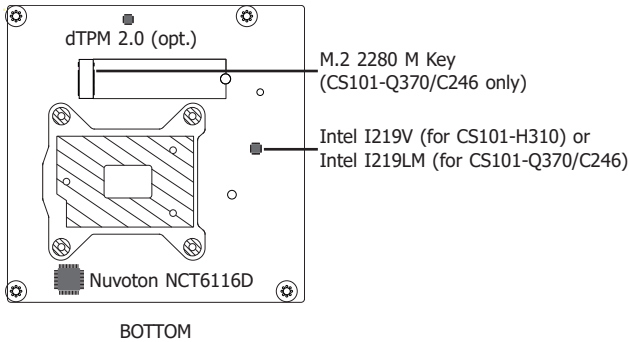


**Board Layout**



TOP

<b>Backlight Brightness Select</b>	<b>JP7</b>	<b>Clear CMOS</b>	<b>JP1</b>
+3.3V (default)	1-3 On	Normal (default)	1-2 On
+5V	3-5 On	Clear CMOS	2-3 On
<b>Mini PCIe/mSATA Select (CS101-Q370/C246 only)</b>	<b>JP7</b>	<b>LCD/Inverter Power Select</b>	<b>JP9</b>
mSATA	2-4 On	+12V (default)	1-2 On
Mini PCIe (default)	4-6 On	+5V	2-3 On
<b>Panel Power Select</b>	<b>JP8</b>	<b>LVDS/eDP Signal Select</b>	<b>JP10</b>
+12V	1-2 On	eDP	1-2 On
+5V	3-4 On	LVDS (default)	2-3 On
+3.3V (default)	5-6 On		



## Pin Assignment

### Front Panel

	Pin	Assignment		Pin	Assignment
	1	NC	PWR-LED	2	LED Power
HD-LED	3	HDD Power		4	LED Power
	5	Signal		6	Signal
RESET	7	GND	ATX-SW	8	GND
	9	Signal		10	Signal
	11	NC		12	---

### SATA 3.0 0(R2)/1(R3)

Pin	Assignment
1	GND
2	TX+
3	TX-
4	GND
5	RX-
6	RX+
7	GND

### COM 1/2 Headers: RS232/422/485

#### COM 3/4 Headers: RS232

Pin	RS232	RS422 Full Duplex	RS485
1	DCD-	TX-	DATA-
2	RD	TX+	DATA+
3	TD	RX+	NC
4	DTR-	RX-	NC
5	GND	GND	GND
6	DSR-	NC	NC
7	RTS-	NC	NC
8	CTS-	NC	NC
9	RI-	NC	NC

### SATA Power

Pin	Assignment
1	+12V
2	GND
3	GND
4	5V

**4-pin Vertical Type (optional)**

Pin	Assignment	Pin	Assignment
1	GND	2	GND
3	+12V	4	+12V

**eDP (optional)**

Pin	Assignment	Pin	Assignment
1	NC	21	eDP Panel PWR
2	GND	22	NC
3	eDP_Lane 3-	23	GND
4	eDP_Lane 3+	24	GND
5	GND	25	GND
6	eDP_Lane 2-	26	GND
7	eDP_Lane 2+	27	Hot Plug
8	GND	28	eDP_GND
9	eDP_Lane 1-	29	eDP_GND
10	eDP_Lane 1+	30	eDP_GND
11	GND	31	eDP_GND
12	eDP_Lane 0-	32	Panel Backlight On/Off Control
13	eDP_Lane 0+	33	Panel Inverter Brightness Voltage Control
14	GND	34	NC
15	eDP_AUX+	35	NC
16	eDP_AUX-	36	Inverter PWR
17	GND	37	Inverter PWR
18	eDP Panel PWR	38	Inverter PWR
19	eDP Panel PWR	39	Inverter PWR
20	eDP Panel PWR	40	NC

**LVDS LCD Panel**

Pin	Assignment	Pin	Assignment
1	GND	2	GND
3	LVDSA_3+	4	LVDSB_3+
5	LVDSA_3-	6	LVDSB_3-
7	GND	8	GND
9	LVDSA_2+	10	LVDSB_2+
11	LVDSA_2-	12	LVDSB_2-
13	GND	14	GND
15	LVDSA_1+	16	LVDSB_1+
17	LVDSA_1-	18	LVDSB_1-
19	GND	20	GND
21	LVDSA_0+	22	LVDSB_0+
23	LVDSA_0-	24	LVDSB_0-
25	GND	26	GND
27	LVDSA_CLK+	28	LVDSB_CLK+
29	LVDSA_CLK-	30	LVDSB_CLK-
31	GND	32	GND
33	DDC_CLK	34	NC
35	DDC_DATA	36	+3.3V
37	Panel Power	38	Panel Power
39	Panel Power	40	Panel Power

**SMBus**

Pin	Assignment	Pin	Assignment
1	3V3SB	2	GND
3	SMB_CLK	4	SMB_DATA
5	SMB_ALERT	6	---

### LCD/Inverter Power

Pin	Assignment
1	GND
2	GND
3	Panel Inverter Brightness Voltage Control
4	Panel Power
5	+3.3V
6	Panel Backlight On/Off Control
7	LCD/Inverter Power
8	LCD/Inverter Power

### Digital I/O

Pin	Assignment
1	DIO7
2	DIO6
3	DIO5
4	DIO4
5	DIO3
6	DIO2
7	DIO1
8	DIO0

### Digital I/O Power

Pin	Assignment
1	+12V
2	GND
3	5VSB
4	+5V

### Battery

Pin	Assignment
1	+3.3V
2	GND

### LPC

Pin	Assignment	Pin	Assignment
1	CLK	2	LAD1
3	RST#	4	LAD0
5	FRAME#	6	3V3
7	LAD3	8	GND
9	LAD2	10	---
11	SERIRQ	12	GND
13	5VSB	14	5V

### Front Audio

Pin	Assignment	Pin	Assignment
1	Mic2-L	2	GND
3	Mic2-R	4	NC
5	Line2-R	6	Mic2-JD
7	GND	8	---
9	Line2-L	10	Line2-JD

### USB 2.0 5-6/7-8 Headers

Pin	Assignment	Pin	Assignment
1	PWR	2	PWR
3	DATA-	4	DATA-
5	DATA+	6	DATA+
7	GND	8	GND
9	---	10	NC

### Chassis Intrusion

Pin	Assignment
1	Signal
2	GND

### CPU Fan

Pin	Assignment
1	GND
2	12V
3	Sense
4	Speed Control



DFI reserves the right to change the specifications at any time prior to the product's release. For the latest information about the product, please refer to the user's manual at [go.dfi.com/CS101](http://go.dfi.com/CS101) (CS101-Q370/C246 and CS101-H310 share the same quick reference and manual).

